



MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE

(UGC - AUTONOMOUS)

(Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi)

An ISO 9001: 2008 Certified Institution; www.mits.ac.in

Examination Section

Schedule - M.Tech – I Year II Semester (R14) Regular & Supplementary End Semester Examinations August – 2016

Academic Year – 2015 – 16

TIMINGS: FN: 10:00AM to 01:00PM

Table with 7 columns: DATE / DAY, Electrical Power Systems (EPS), Solar Power Systems (SPS), Advanced Manufacturing Systems (AMS), Digital Electronics & Communication Systems (DECS), VLSI & Embedded Systems (VES), Computer Science & Engineering (CSE). Rows list exam dates from 01.08.16 to 13.08.16 with corresponding subjects and codes.

Note: (i) Any clashes or omissions in the time table may please be informed to the Controller of Examinations Immediately.

(ii) Even if Government declares holiday on any of the above dates, the examination shall be conducted as usual.

Sri Kumar V

CONTROLLER OF EXAMINATIONS

Date: 28.06.2016

Copy to: P.A to Principal

- All the HODs for circulation among the staff members and display on notice board, Assistant Controllers of Examinations, Senior Administrative Officer, Placement Officer, Transport Incharge

Signature of Principal

PRINCIPAL

Principal

Madanapalle Institute of Technology & Science MADANAPALLE